

Notice of References Cited	Application/Control No. 09/434,736		Applicant(s)/Patent Under Reexamination KIM ET AL.	
	Examiner Evan T. Pert		Art Unit 2813	Page 1 of 1

U.S. PATENT DOCUMENTS

*	A	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
<input type="checkbox"/>	A	5,320,979	Jun. 1994	Hashimoto et al.	--	--	<input checked="" type="checkbox"/>	<input type="checkbox"/>
<input checked="" type="checkbox"/>	B	5,069,749	Dec. 1991	Gutierrez	--	--	<input checked="" type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	C	4,999,318	Mar. 1992	Takahumi et al.	--	--	<input checked="" type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	D	4,987,099	Jan. 1991	Flanner	--	--	<input checked="" type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	E	4,349,584	Sep. 1982	Flatley et al.	--	--	<input checked="" type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	F	3,769,109	Oct. 1973	MacRae et al.	--	--	<input checked="" type="checkbox"/>	<input type="checkbox"/>
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FOREIGN PATENT DOCUMENTS

*	A	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
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<input type="checkbox"/>	T							<input type="checkbox"/>	<input type="checkbox"/>

NON-PATENT DOCUMENTS

*	A	DOCUMENT (Including Author, Title Date, Source, and Pertinent Pages)	DOCUMENT SOURCE **	
			APS	OTHER
<input type="checkbox"/>	U	Wolf, SILICON PROCESSING FOR THE VLSI ERA VOLUME 2: PROCESS INTEGRATION, 1990, Lattice Press, Chapter 4 (pages 176-297).	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input type="checkbox"/>	V		<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	W		<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	X		<input type="checkbox"/>	<input type="checkbox"/>

*A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a).)

**APS encompasses any electronic search i.e. text, image, and Commercial Databases.

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Notice of References Cited

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